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## U.S. **UTILITY** Patent Application

<b>O.I.P.E.</b>   <b>SCANNED</b>  	<b>PATENT DATE</b>
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT 37281 37281	EXAMINER DAVID TUAN NGUYEN
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# ABANDONED

Low profile integrated module interconnects and method of fabrication

PTO-2040  
12/89

**ISSUING CLASSIFICATION**

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____  _____  _____  _____  _____			ISSUE FEE	
	_____  _____  _____  _____  _____			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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